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Examiner Initials	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journel, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	L3
R	1	Dr. Luu Nguyen et al, Assembly Considerations for micro SMD Wafer-Level CSPs, Chip Scale Review, May/June 2000, page 48 et seq.	
R	2	James L. Young, Wafer-Level Processing: Working Smarter, Chip Scale Review, May 1997, p. 28 et seq.	-
A.	3	Dr. Philip Garrou, Wafer-Level Packaging Has Arrived, Semiconductor International, October 2000, p. 119 et seq.	
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